

Aries 6 Smart DSP Retimer MCIO Add-in Card (Solstice 6)

Introduction

Aries 6 Smart DSP Retimer MCIO add-in card (Solstice 6) is designed for seamless in-system evaluation of the Aries 6 Smart DSP Retimer and interoperability testing with major PCIe 6.x and CXL 3.x hardware vendors. The full-height add-in card (AIC) has a x16 PCIe 6.x CEM-compliant edge connector to plug into a PCIe 6.x host system and features a 124-pin right angle MCIO connector (SFF-TA-1016) to cable to an external endpoint. With seamless plug-and-play operation, the root complex (e.g. CPU) and endpoint (e.g. GPU) will automatically form a link through the Aries 6 Smart DSP Retimer on power-up and de-assertion of PERST#. COSMOS suite for extensive Link, Fleet and RAS Management comes integrated with C and Python SDKs for rapid integration and customization of advanced in-band (PCIe) and out-of-band (I2C/I3C) diagnostics.

Refer to *Aries PCIe®/CXL® Smart DSP Retimer Product Brief* for more details.

Technical Information

Feature	Specification
Part Number	SOLSTICE6-REVA
Form Factor	Standard full-height, half-length, single slot width PCIe AIC (PCIe CEM R6.0.0, Draft 0.7)
Retimer	Aries 6 PCIe/CXL Smart DSP Retimer
Connectors	PCIe CEM 6.x (v 0.7) x16 edge finger, SFF-TA-1016 124 pin right angle MCIO socket
LED Indicators	Yellow: Retimer reset, PCIe fundamental reset Orange: Retimer heartbeat, link status Green: Power good, firmware load done
Bifurcation Options	1x16, 2x8, 4x4, 8x2, others available via COSMOS embedded software
Debug Headers	3.3 V SMBus (Retimer), 3.3 V I2C (EEPROM), 3.3 V SPI (Flash)

Applications

- Design and evaluation of Aries 6 Smart DSP Retimer for AI accelerator baseboard, AI headnode, general purpose servers, CXL memory disaggregation, storage, and other PCIe/CXL based system
- Solstice 6 AIC extends PCIe signal reach for rapid system deployment allowing easy plug-and-play implementation for GPUs, NICs, switches, and other topologies that utilize an MCIO interface

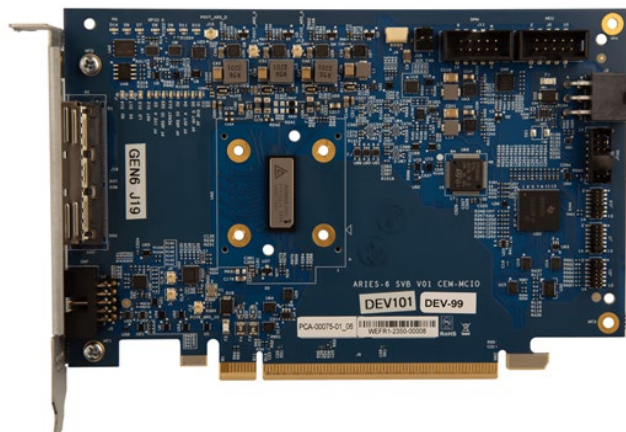
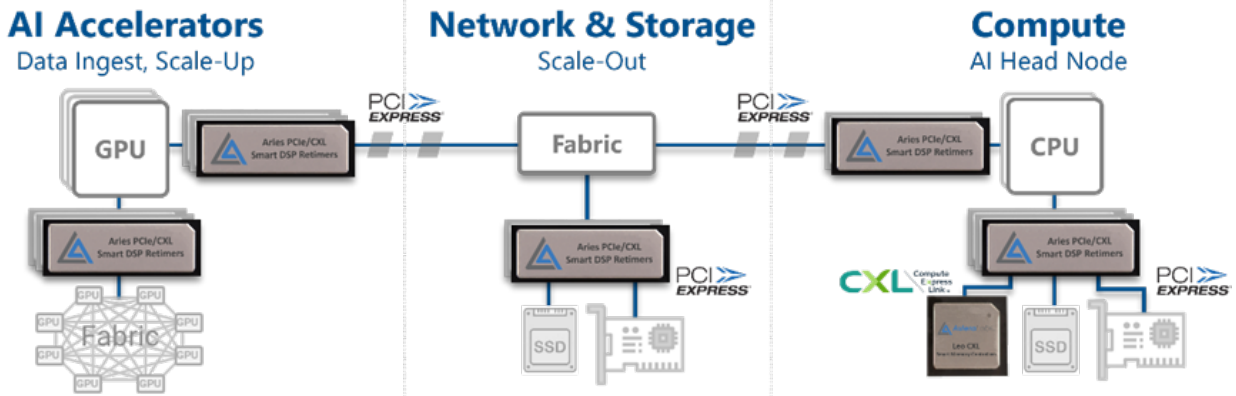


Figure 3-1: Solstice 6 Add-in Card

AI Server Platform Application

The Aries PCIe®/CXL® Smart DSP Retimer portfolio provides robust PCIe connectivity in AI server platforms with complex topologies, densely packed signals that travel longer distances, and an increasing number of connectors that are utilized to expand configuration optionality.



Astera Labs' Intelligent Connectivity Platform

The Aries PCIe®/CXL® Smart DSP Retimer portfolio is an integral part of Astera Labs' Intelligent Connectivity Platform. In addition to Aries, there are multiple PCIe, CXL and Ethernet ICs, Modules & Boards that are at the foundation of the platform. Astera Labs' Connectivity System Management and Optimization Software (COSMOS) suite provides extensive Link, Fleet and RAS Management features and diagnostics. Through the combination of software and hardware, the Intelligent Connectivity Platform enables a smart connectivity backbone that is both scalable and customizable for AI and cloud infrastructure.

