

**Position: Product Applications Engineering****Location: San Francisco Bay Area****Overview:**

Astera Labs Inc., a leader in purpose-built connectivity solutions for data-centric systems, is seeking a **Product Applications Engineer** in Santa Clara, CA with experience supporting the development and designing in of semiconductor products for high-speed communication protocols like PCIe, Ethernet, Infiniband, DDR, NVMe, USB, etc.

**Job Description:**

As an Astera Labs Product Application Engineer, you will be part of a team that designs-in and supports Astera Labs' portfolio of connectivity products in the world's leading cloud service providers, server and network OEMs. In this role, you will need to provide technical guidance to customers to overcome design challenges, generate collateral for existing and new products, and drive innovation by providing insightful feedback to other internal teams to continuously improve products and processes. There are opportunities to support key customers directly, and also to dive deep in the lab to address the challenges associated with leading edge semiconductor products.

**Basic qualifications:**

- Strong academic and technical background in electrical engineering. At minimum, a Bachelor's in EE is required, Master's is preferred.
- Minimum of 3 years' experience working with Cloud service providers and server OEM customers to design in complex SoC/silicon products for Server, Storage, and/or Networking applications.
- Entrepreneurial, open-mind behavior and can-do attitude. Think and act with the customer in mind!

**Required experience:**

- Hands-on, thorough knowledge of NRZ/PAM4 SerDes-based protocols like PCIe, Ethernet (25G and above), etc. and/or memory interfaces such as (LP)DDR5/4/3.
- Silicon/System bring-up and debug experience in customer systems.
- Firsthand experience with lab equipment including protocol analyzers and high-speed oscilloscopes.
- A strong background in high-speed board design techniques, and understanding of Data Center systems like Servers, JBOGs/JBODs, Networking switches/routers etc.
- Technical writing skills to generate clear, precise documentation including datasheets, application notes, and similar guides for internal and customer-facing audiences

**Preferred experience:**

- Development/support for PCIe or Ethernet Switch products.
- Working with silicon characterization/validation teams to ensure device performance is readily achievable in customer systems
- Firmware development with C-language, scripting with Python or other equivalent programming languages.
- Knowledge of simulation/modeling, schematic capture, and PCB layout tools from Cadence, Altium and others.
- Knowledge of simulation tools such as Keysight ADS, SiSoft QCD, etc. for IBIS-AMI analysis.